

INVITATION FOR BID (IFB)
(LOCAL COMPETITIVE BIDDING)

- 1.0** The Maharashtra State Electricity Transmission Company Ltd., hereinafter referred to as the 'Owner' (MSETCL) invites offers through SRM E-Tendering in TWO bid system (Techno- commercial bid and price bid) from the eligible registered bidders for the following scope as detailed below.

SRM E-Tender No. – T-12 / 2021-22 / SE / EHV O&M / PUNE

Sr. No.	Description of Work	Completion period	Estimated Cost (Rs.)
RFx No. : 7000020785			
1	Supply, erection, testing & commissioning of rapid restoration system (RRS) for ICTs at 400kV Lonikand-I & 400kV Chakan Substations under EHV O&M Circle, Pune.	09 months from the date of issue of work order.	Rs.2,48,80,067/-

Sr. No.	Particulars	Date & Amount
1	Date and time for sale of bid documents	From Dt.02.09.2021, 11.00 Hrs.
2	Last date and time for submission of bids	On or before Dt.09.09.2021 up to 11.00 Hrs.
3	Date and time for opening of techno-commercial bid	Dt. 10.09.2021 at 11.00 Hrs. if possible.
4	Date and time for opening of Price Bids	Dt. 13.09.2021 at 11.00 Hrs. if possible.
5	Price of bid document (SRM E-Tender Fees)	Rs. 5000/- + GST
6	Earnest Money Deposit (EMD) (1% of estimated cost)	Rs.2,48,800.67/-

Bidders are requested to note the dates for purchase, submission and opening of the bids which are as above.

In case the due date for opening of bids happens to be a holiday, the next working day shall be the corresponding due date.

The prices to be quoted against this IFB shall be in Indian rupees only.

The tender is invited on percentage basis.

For Evaluation of the Tender, the total price of (Supply + ETC + Civil) works including GST & Duties shall be considered.

It should be noted by the Bidders that, Labour welfare cess at the rate of 1% of total amount of contract (Supply + ETC + Civil) will be deducted from the bills.

The LOA/work order will be placed on final accepted price for (Supply + ETC + Civil) works including GST & Duties.

For extra quantities if any, rate shall be as per approved latest MSETCL SOR or as reasonable.